1/4 B.Tech SECOND SEMESTER

IT2T4 ELECTRONIC DEVICES AND CIRCUITS Credits: 4

Lecture: 4 periods/week Internal assessment: 30 marks
Tutorial: 1 period /week Semester end examination: 70 marks

Objectives:

- To study in detail about construction of several electronic devices.
- To analyse the characteristics of various electronic devices and circuits.
- To get familiarize in biasing and stabilization concepts.

Outcomes:

The student will be able to

- Understand the semiconductor physics of the intrinsic, p and n materials.
- Understand amplifier circuits, oscillators and filter circuits employing BJT, FET devices.
- Understand the function and operation of diodes, transistors and amplifiers.

Syllabus:

UNIT - I

Semi Conductors Conductors, Semiconductors, Silicon Crystals, Intrinsic Semiconductors, Two Types of flow, Doping a Semi Conductor, Two types of Extrinsic Semi Conductors, The Unbiased Diode, Forward Bias, Reverse Bias, Breakdown, Energy Levels, The Energy Hill, Barrier Potential and Temperature, Reverse – Biased Diode.

Diode Theory Basic Ideas, The ideal Diode, Second and Third Approximations, Calculation of Bulk Resistance, DC Resistance of a Diode, Load lines and Surface – Mount Diodes.

UNIT - II

Diode Circuits Half-Wave Rectifier, Transformer, Full-Wave Rectifier, Bridge Rectifier, Choke-Input Filter, Capacitor-Input Filter, Peak Inverse Voltage and Surge Current, Special-Purpose Diodes Zener Diode, Zener Regulator, Zener Drop-Out Point, Data Sheet, Light – Emitting Diode(LED), Photodiode, Tunnel Diode and PIN Diodes.

UNIT- III

Bipolar Junction Transistors Unbiased and Biased Transistor, Transistor currents, Common Emitter Connection, Based Curve, Collector Curves, Transistor Approximations, Transistor fundamentals Variations in current Gain, Load Line, Operating Point, Recognizing Saturation, Transistor Switch, Emitter Bias, LED Drivers, The Effect of Small Changes.

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UNIT - IV

Transistor Biasing Voltage – Divider Bias, Accurate VDB Analysis, VDB Load Line and Q Point, Two – Supply Emitter Bias, Other Types of Bias, PNP Transistors. AC Models Base – Biased Amplifier, Emitter – Biased Amplifier, Small Signal Operation, AC Beta, AC Resistance of the Emitter Diode, Two Transistor Models.

UNIT - V

Amplifiers Voltage Gain, Loading Effect of Input Impedance, Multistage Amplifier, Swamped Amplifier, Two – Stage Feedback, CC Amplifier, Output Impedance, Cascading CE and CC, Darlington Connections, Voltage Regulation and Common – Base Amplifier.

UNIT - VI

JFETS Basic Ideas, Drain Curves, TransConductance curve, Biasing in the Ohmic Region and in the Active Region, Transcondcutance, JFET Amplifier, JFET Analog Switch, Other JFET Applications.

UNIT - VII

MOSFETs The Depletion – Mode MOSFET, D-MOSFET Curves, Depletion – Mode MOSFET Amplifiers, Enhancement – Mode MOSFET, Ohmic Region, Digital Switching, CMOS, E-MOSFET Amplifiers Thyristors The Four-Layer Diode, Silicon Controlled Rectifier.

UNIT - VIII

Frequency Response of Amplifiers Frequency Response of an Amplifier, Decibel Power Gain, Decibel Voltage Gain, Impedance Matching, Decibels above a Reference and Bode Plots.

Text Book:

1. Electronic Principles, Albert Malvino and David J Bates, 7th Edition, Tata McGraw – Hill.

Reference Books:

- Electronic Devices and Circuit Theory, Robert L.Boylestad and Louis Nashelsky, 10th Edition(2010), Pearson/PHI
- 2. Integrated Electronics, Jacob Millman, Christos C. Halkias & Chetan Parikh, 2nd Edition(2011), Tata McGraw Hill.